2.5V Drive Nch+Nch MOS FET UM6K1N

Structure

Silicon N-channel MOS FET

Features

- 1) Two 2SK3018 transistors in a single UMT package.
- 2) The MOS FET elements are independent, eliminating mutual interference.
- 3) Mounting cost and area can be cut in half.
- 4) Low On-resistance.
- 5) Low voltage drive (2.5V drive) makes this device ideal for portable equipment.

Applications

Interfacing, switching (30V, 100mA)

Packaging specifications

	Package	Taping	
Туре	Code	TN	
	Basic ordering unit (pieces)	3000	
UM6K1N		0	

•Absolute maximum ratings (Ta=25°C)

<It is the same ratings for Tr1 and Tr2.>

Parameter		Symbol	Limits	Unit	
Drain-source voltage		VDSS	30	V	
Gate-source voltage		Vgss	±20	V	
Drain current	Continuous	ID	±100	mA	
	Pulsed	I _{DP} *1	±400	mA	
Total power dissipation		P _D *2	150	mW	
Channel temperature		Tch	150	°C	
Range of storage temperature		Tstg	-55 to +150	°C	

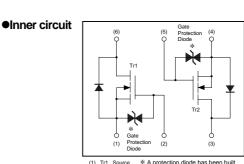
∗1 Pw≤10μs, Duty cycle≤1%

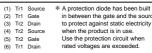
*2 With each pin mounted on the recommended lands.

Thermal resistance

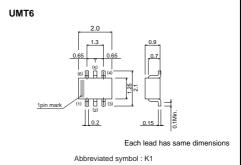
Parameter	Symbol	Limits	Unit
Channel to ambient	Rth(ch-a)*	833	°C / W / TOTAL
	Rtn(cn-a)	1042	°C / W / ELEMENT

* With each pin mounted on the recommended lands.





•External dimensions (Unit : mm)



Transistors

•Electrical characteristics (Ta=25°C)

<It is the same characteristics for Tr1 and Tr2.>

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Gate-source leakage	lgss	-	-	±1	μA	Vgs=±20V, Vps=0V
Drain-source breakdown voltage	V(BR)DSS	30	-	-	V	ID= 10μA, Vgs=0V
Zero gate voltage drain current	IDSS	-	-	1.0	μA	VDS= 30V, VGS=0V
Gate threshold voltage	V _{GS (th)}	0.8	-	1.5	V	V _{DS} = 3V, I _D = 100µA
Static drain-source on-state resistance	R _{DS} (on)	-	5	8	Ω	I _D = 10mA, V _{GS} = 4V
		-	7	13	Ω	I _D = 1mA, V _{GS} = 2.5V
Forward transfer admittance	Y _{fs}	20	-	-	mS	I _D = 10mA, V _{DS} = 3V
Input capacitance	Ciss	-	13	-	pF	V _{DS} = 5V
Output capacitance	Coss	-	9	_	pF	V _{GS} =0V
Reverse transfer capacitance	Crss	-	4	-	pF	f=1MHz
Turn-on delay time	t _{d (on)}	-	15	-	ns	Vdd≒5V
Rise time	tr	-	35	_	ns	ID= 10mA Vgs= 5V
Turn-off delay time	td (off)	-	80	_	ns	$R_{L}=500\Omega$
Fall time	t _f	_	80	_	ns	R _G =10Ω

Electrical characteristic curves

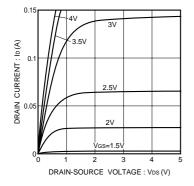


Fig.1 Typical Output Characteristics

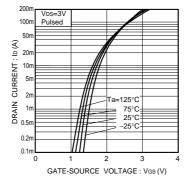


Fig.2 Typical Transfer Characteristics

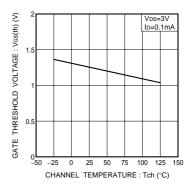


Fig.3 Gate Threshold Voltage vs. Channel Temperature

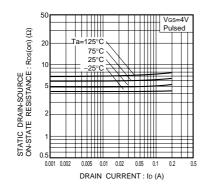


Fig.4 Static Drain-Source On-State Resistance vs. Drain Current (I)

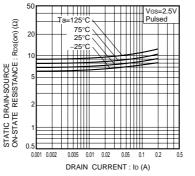


Fig.5 Static Drain-Source On-State Resistance vs. Drain Current (II)

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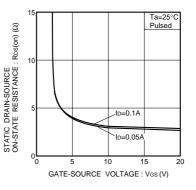


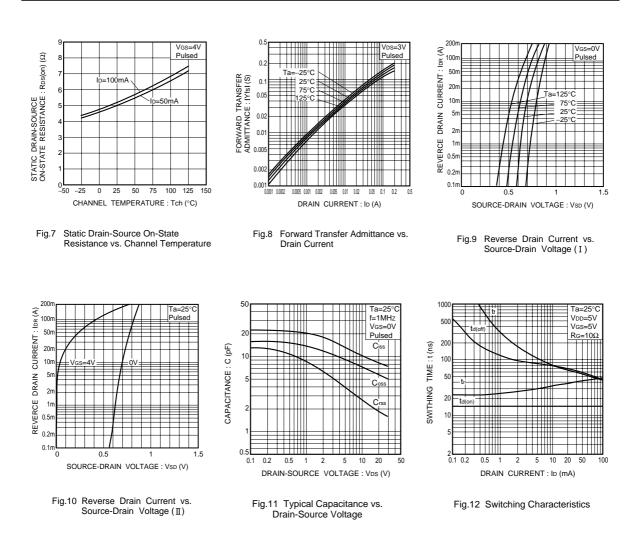
Fig.6 Static Drain-Source On-State Resistance vs. Gate-Source Voltage

Rev.B

2/3

UM6K1N

Transistors



•Switching characteristics measurement circuit

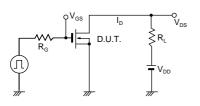


Fig.13 Switching Time Test Circuit

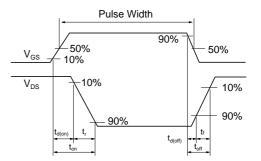


Fig.14 Switching Time Waveforms

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